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Ap100 Global Download Free 2017 AMD Phenom II X6 945K If you have looked at the clock speed of modern processors, it's no surprise that AMD's latest line of Zen-based processors, including the Ryzen 7 and the Ryzen 5, have a better clock speed than Intel's even 10-core CPUs. But it's not only about how fast the CPUs are. Time magazine noted that the sheer number of cores in the Zen CPU is what's making it stand out. So let's look at what makes AMD's new processors so special. First of all, the Zen architecture itself is different than Intel's. While Zen CPUs still make use of the same integer and floating point hardware as previous-generation CPUs, the Zen CPUs don't have separate execution units for floating point operations (FPUs) and integer operations. . The Zen architecture uses more of the same hardware (X86 ISA) for both integer and floating point operations, with an additional CPU execution unit to run integer math. The idea of increasing the number of cores was to use the same processor hardware for both integer and floating point calculations. With Zen, AMD has simply repurposed that hardware for integer calculations. Those integer operations are inherently available to x86 processors, and they don't require a separate set of hardware. An intelligent hypervisor of course requires a full set of execution units for both integer and floating point operations, but that's all AMD's Zen architecture needs. AMD's Zen CPUs incorporate a few enhancements as well. Intel's Haswell CPUs introduced the AVX-512 instruction set, a new instruction set designed to help CPUs crunch numbers. Intel CPUs based on the Skylake microarchitecture will use AVX-512 instructions. AMD's Zen CPUs, however, use the AVX-512 instruction set as well, and feature a bit more in the way of enhanced performance. AVX-512 increased the number of registers the CPU can use for a variety of calculations. The AMD Phenom II X6 945K supports the AVX-512 instruction set. It supports 256 double precision registers. 16 of the 64 bit registers can be used for half precision operations. One can use VEX instructions, for example, to perform 32 bit operations on the FP registers. Further, one can use other instructions for narrower operations. . AMD's Ryzen models start at 6 cores, so it's not surprising that AMD has increased its cache size. The

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